

APPLICATION NO FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/768,580	01/23/2001	John F. McMahon	42390.P5142D 3565			
7590 12/22/2003			EXAMINER			
REAKTEY SOKOLOFF TAVEOR & ZAFMANLER			CSTAMBLE	CSTAMBLING ALCOHOL		

Seventh Floor 12400 Wilshire Boulevard

Los Angeles, CA 90025-1026

PAPER NUMBER

ART UNIT 2827

DATE MAILED: 12/22/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Application	n No.	lo. Applicant(s)				
		09/768,58	0	MCMAHON, JOHN F.				
		Examiner		Art Unit				
		Alonzo Ci		2827				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REFLY IS SET TO EXPIRE 2 MONTH(S) FROM THE MALLING DATE OF THIS COMMUNICATION.  If EMAILING DATE OF THIS COMMUNICATION (I) is the sate, those up a not by te interp. Find the sate of the communication of the								
1)⊠	Responsive to communication(s) filed on	08 October 2003	ł.					
2a)⊠	This action is FINAL. 2b)	This action is no	n-final.					
3)[	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims								
4)⊠	Claim(s) 28-40 is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)	Claim(s) is/are allowed.							
6)🖂	6)⊠ Claim(s) <u>28-40</u> is/are rejected.							
7)	Claim(s) is/are objected to.							
8)	Claim(s) are subject to restriction a	ind/or election re	quirement.					
Application Papers								
<ol> <li>The specification is objected to by the Examiner.</li> </ol>								
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.								
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
<ol> <li>The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.</li> </ol>								
Priority under 35 U.S.C. §§ 119 and 120								
12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) ☐ All b) ☐ Some * ci ☐ None of: 1. ☐ Certified copies of the priority documents have been received.								
Certified copies of the priority documents have been received in Application No								
<ol><li>Copies of the certified copies of the priority documents have been received in this National Stage</li></ol>								
application from the International Bureau (PCT Rule 17.2(a)).  * See the attached detailed Office action for a list of the certified copies not received.								
13) Acknowledgment is made of a claim for domestic priority under 35 U S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.								
The translation of the foreign language provisional application has been received.								
14) Acknowledgment is made of a daim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.								
Attachment(s)								
1) Notice	of References Cried (PTO-892)		(i) Interview Summary (ii	PTO-413) Paper Nor	a)			
2) Notice 3) Inform	e of Draftsperson's Patent Drawing Review (PTO-948 nation Disclosure Statement(s) (PTO-1449) Paper No	3)	5) Notice of Informal Pa 5) Other:	tent Application (PTC	1-152)			

Application/Control Number: 09/768,580 Art Unit: 2827

# DETAILED ACTION

 Amendment E filed on 10/8/03 has been fully considered and made of record in Paper No. 15.

## Response to Arguments

Applicant's arguments filed 10/8/03 have been fully considered but they are not persuasive.

Contrary to applicant's remarks Templeton disclose a substrate attached to a plurality of shelves. Templeton does not explicitly disclose that the substrate side that does not have the flip-chip mounted to it is mounted to the plurality of shelves.

However, one skilled in the art at the time of the invention would have readily recognize flip the substrate so that the side that does not have the flip-chip mounted to it is mounted to the plurality of shelves, since by flipping the substrate the flip chip can be electrically connected to the plurality of shelves without the use of a via, which would shorter the electrical distance between devices. Therefore, it would have been obvious to incorporate a substrate so that the side that does not have the flip-chip mounted to it is mounted to the plurality of shelves with the process of Templeton, since by flipping the substrate the flip chip can be electrically connected to the plurality of shelves without the use of a via, which would shorter the electrical distance between devices. Furthermore, Ueda discloses that chip devices can be mounted facing towards the cavity or facing outside of the cavity as shown in Figs. 4 and 6.

### Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all
obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though he invention is not identically disclosed or described as set forth in section 102 of this tife, if the differences between the subject matter sought to be patentialed and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains, Patentability shall not be negatived by the manner in which the invention was made.

 Claims 28, 29, 31-33, and 35-40-40 are rejected under 35 U.S.C. 103(a) as being unpatentable over Templeton, Jr. et al. (U.S. 5,874,321).

With respect to Claim 28, Templeton discloses electrically connecting a semiconductor die 704 to at least one of a plurality of shelves 702-1 - 702-N, electrically connecting a flip-chip 7033-703c to a ceramic substrate 706, and attaching the ceramic substrate 706 to one of the plurality of shelves 702-1 -702-N (see col. 9 lines 44-67 and col. 10 lines 1-58°, Fig. 7). Templeton does not explicitly disclose that the substrate side that does not have the flip-chip mounted to it is mounted to the plurality of shelves. However, one skilled in the art at the time of the invention would have readily recognize flip the substrate so that the side that does not have the flip-chip mounted to it is mounted to the plurality of shelves, since by flipping the substrate the flip chip can be electrically connected to the plurality of shelves without the use of a via, which would shorter the electrical distance between devices. Therefore, it would have been obvious to incorporate a substrate so that the side that does not have the flip-chip mounted to it is mounted to the plurality of shelves with the process of Templeton, since by flipping the substrate the flip chip can be electrically connected to the plurality of shelves without the use of a via, which would shorter the substrate the flip chip can be electrically connected to the plurality of shelves without the use of a via, which would shorter the electrical distance between devices.

With respect to Claim 29, Templeton discloses electrically connecting the ceramic substrate 706 to at least one of the plurality of shelves 702-1 - 702-N (see Fig. 7).

With respect to Claim 31, Templeton discloses wherein attaching the ceramic substrate 706 to one of the plurality of shelves 702-1-702-N provides a lid above the semiconductor die 704 (see Fin. 7).

With respect to Claim 32, Templeton discloses electrically testing the electrically connected flip-chip 703s-703c before attaching of the ceramic substrate 706 to one of the plurality of shelves 702-1 - 702-N (see col. 10 lines 59-65).

With respect to Claim 33, Templeton discloses wherein electrically connecting the flip-chip 703a to the ceramic substrate 706 comprises electrically connecting the flip-chip 703a to the ceramic substrate 706 with solder balls (see col. 10 lines 3-10).

With respect to Claim 34, Templeton discloses disposing a seal 712 between a base of the ceramic substrate 706 and one of said plurality of shelves (i.e. 702C) to which the ceramic substrate 706 is attached (see Fig. 7).

With respect to Claims 36 and 37, Templeton discloses wherein electrically connecting the semiconductor die 301 to the at least one of a plurality of shelves comprises electrically connecting a CPU chip to the at least one of the plurality of shelves. Also, wherein electrically connecting the flip-chip 308 to the ceramic substrate 302 comprises electrically connecting a memory cache (i.e. Memory chips) flip-chip 308 to the ceramic 302 (see cot 5 lines 61-67; Fiss.3A and 4).

With respect to Claim 38, Templeton discloses wherein electrically connecting the semiconductor die 704 to at least one of the plurality of shelves 702-1 - 702-N comprises electrically connecting the semiconductor die 704 to the at least one of a plurality of shelves 702-1 - 702-N with at least one bond wire (see col. 10 lines 3-10).

With respect to Claims 39 and 40, Templeton discloses including attaching the semiconductor die 301 to a heat stug 305 (i.e. heat sink), wherein the heat stug 305 is attached to at least one of a plurality of shelves (see col. 5 lines 4-15 and 38-44., Fig. 3A).

 Claims 30 and 34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Templeton, Jr. et al. (U.S. 5,874,321) as applied to claim 28 above, and further in view of Gaudenzi et al. (U.S. 5,313,366).

With respect to Claim 30, Templeton fails to disclose wherein electrically connecting the ceramic substrate to at least one of said plurality of shelves comprises electrically connecting the ceramic substrate to at least one of the plurality of shelves electrically connecting the ceramic substrate to at least one of the plurality of shelves with at least one bond wire. However, Gaudenzi discloses wherein electrically connecting the ceramic substrate [i.e. chip carrier) 150 to at least one of the plurality of shelves comprises electrically connecting the ceramic substrate 150 to at least one of the plurality of shelves with at least one bond wire 152 (see col. 3 lines 46-61 and col. 4 lines 50-64;Fig. 4B). Therefore, it would have been obvious to incorporate wire bonding the substrate to a plurality of shelves with the process of Templeton, since the wire bonding would facilitate a direct electrical connection between the substrate (i.e. chip carrier) with the plurality of shelves as taught by Gaudenzi.

With respect to Claim 34, Gaudenzi discloses covering the flip chip 106 with an encapsulant 142 (see col. 4 lines 24-35;Fig. 3B).

The prior art made of record and not relied upon is cited primarily to show the process of the instant invention.

#### Conclusion

 Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filled within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the state of this final action.

Any inquiry concerning the communication or earlier communications from the examiner should be directed to Alonzo Chambliss whose telephone number is (703) 306-9143. The fax phone number for this Group is (703) 308-7722 or 7724. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-7956.

AC/December 15, 2003

Alonzo Chambliss Patent Examiner Art Unit 2827